

(Use several sheets if necessary)

AD-320J

Application Number

Application Number
19824,012

Applicant(s)

Wu et al.

Filing Date

4/14/04

Group Art Unit,

2816



U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	<p>Williams, et al. "Third-Order Sigma-Delta Modulator with Extended Dynamic Range", IEEE Journal of Solid-State Circuits, Vol. 29, No. 3, March 1994, pages 193 - 202</p> <p>Grilo et al., "Predictive Correlated Double Sampling Switched-Capacitor Integrators", 1998 IEEE, pages 9 - 12</p>
	<p>Huang et al. "Reduced Nonlinear Distortion In Circuits with Correlated Double Sampling", IEEE Transactions on Circuits and Systems -II: Analog and Digital Signal Processing, Vol. 44, No .7, July 1997, pages 593 -597</p> <p>Nagaraj, et al. "Reduction of Finite-Gain Effect In Switched Capacitor Filters", Electronics Letter, July 4, 1985, Volume 21, Number 14</p>

EXAMINER

WELLS

DATE CONSIDERED

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